

The background of the book cover is a high-magnification, false-color aerial photograph of a complex integrated circuit (IC) die. The die is rectangular and filled with a dense, intricate pattern of circuitry. Various regions are highlighted in different colors: bright green, red, blue, purple, and yellow, which likely represent different functional blocks or materials used in the chip's construction. The overall appearance is that of a highly detailed, modern microchip.

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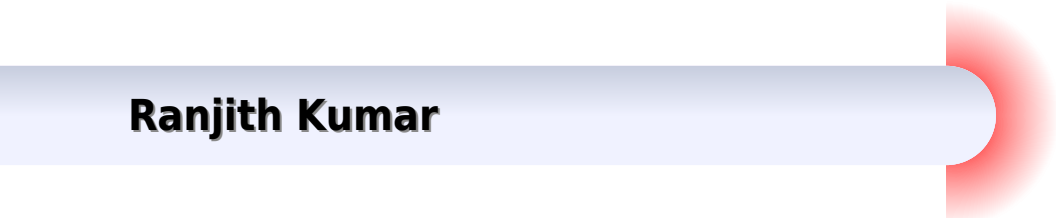
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# Cmos Vlsi Design Weste Solution Manual

**Ranjith Kumar**



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