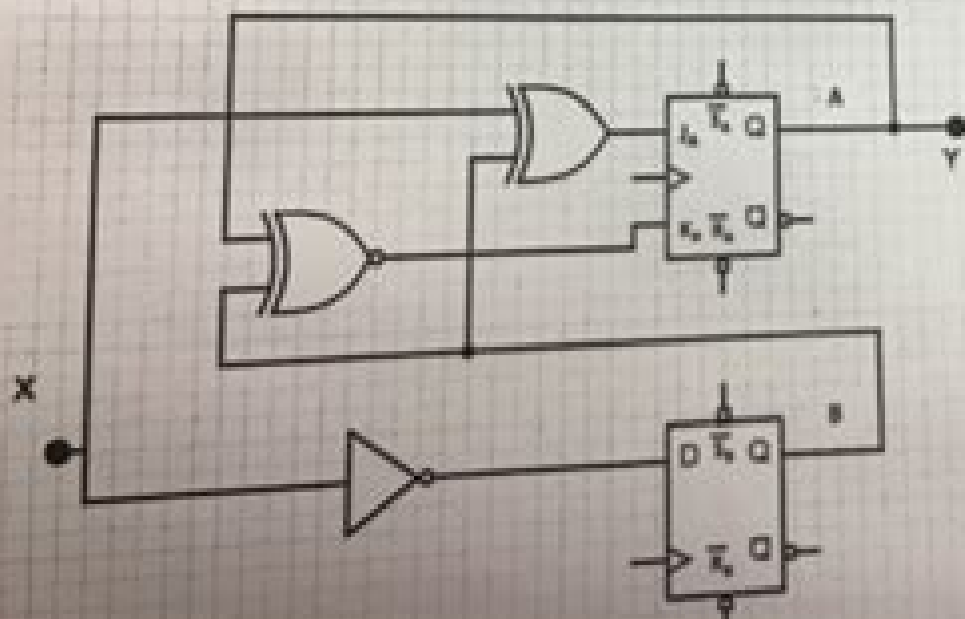


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Problem 5 (25 pts) FSM Circuit Timing Diagram and Re-Design.

- (10 pts) Draw the state transition diagram of the FSM represented by the following circuit.
- (10 pts) Draw the wave forms of A, B, A*, B* in following timing diagram for the following circuit.
- (5 pts) Modify the circuit so that the modified circuit would implement the same FSM by replacing the D flip-flop (that produces B) in the circuit by a J-K flip-flop (with inputs J_B and K_B and it will also generate B). You need to first write down a truth table to show how J_B and K_B depend on X and B, and then write down two Boolean equations of J_B and K_B as functions of X and B or just X. You do not need to re-draw the circuit.



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