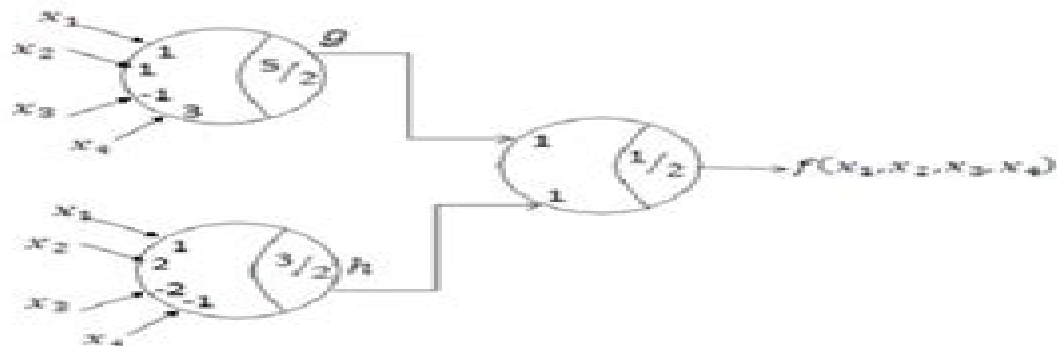


DIGITAL SWITCHING AND LOGIC DESIGN

QUESTION BANK

CHAPTER1: Threshold Logic

- Q1. Is it possible to design EX-OR gate with a single threshold element? Justify your answer.
- Q2. Let $f(x_1, x_2, x_3) = \sum(0,1,3)$. Realize it using a threshold element.
- Q3. What are the various capabilities of threshold logic?
- Q5. Determine whether the function $f(x_1, x_2, x_3, x_4) = \sum(0,1,3,4,5,6,7,12,13)$ is a threshold function, and, if it is, find a weight-threshold vector.
- Q6. By examining the linear inequalities, determine which of the following functions is a threshold function, and for each one that is, find the corresponding weight-threshold vector.
- $f_1(x_1, x_2, x_3) = \sum(1,2,3,7)$
 - $f_1(x_1, x_2, x_3) = \sum(0,2,4,5,6)$
 - $f_1(x_1, x_2, x_3) = \sum(0,3,5,6)$
- Q7. Determine the function $f(x_1, x_2, x_3, x_4)$ realized by the network as shown in fig below.



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begins with an overview of the essential laws of Boolean algebra K map plotting techniques as well as the simplification of Boolean functions This text then presents the properties and develops the characteristic equations of a number of various types of flip flop Other chapters consider the design of synchronous and asynchronous counters using either discrete flip flops or shift registers This book discusses as well the design and implementation of event driven logic circuits using the NAND sequential equation The final chapter deals with simple coding techniques and the principles of error detection and correction This book is a valuable resource for undergraduate students digital engineers and scientists Digital Logic Techniques John Stonham,2017-11-22 The third edition of Digital Logic Techniques provides a clear and comprehensive treatment of the representation of data operations on data combinational logic design sequential logic computer architecture and practical digital circuits A wealth of exercises and worked examples in each chapter give students valuable experience in applying the concepts and techniques discussed Beginning with an objective comparison between analogue and digital representation of data the author presents the Boolean algebra framework for digital electronics develops combinational logic design from first principles and presents cellular logic as an alternative structure more relevant than canonical forms to VLSI implementation He then addresses sequential logic design and develops a strategy for designing finite state machines giving students a solid foundation for more advanced studies in automata theory The second half of the book focuses on the digital system as an entity Here the author examines the implementation of logic systems in programmable hardware outlines the specification of a system explores arithmetic processors and elucidates fault diagnosis The final chapter examines the electrical properties of logic components compares the different logic families and highlights the problems that can arise in constructing practical hardware systems **A TEXTBOOK OF DIGITAL LOGIC CIRCUITS (With Excellent Question Bank for University & Competitive Exams)** Na Vikraman,2020-06-07 This book has been written for the All year of BE B Tech students of All University with latest syllabus for ECE Digital Electronics EEE Digital Logic Circuits CSE IT Digital Principles and System Design The basic aim of this book is to provide a basic knowledge in Digital Electronics Digital Logic Circuits Digital Principles and System Design for engineering students of degree diploma AMIE courses and a useful reference for these preparing for competitive examinations All the concepts are explained in a simple clear and complete manner to achieve progressive learning Two marks questions and answers Short Long answer questions are provided at the end of fifth chapter This book is divided into five chapters Each chapter is well supported with the necessary illustration practical examples and solved problems Digital Logic Design John F. Passafiume,Michael Douglas,1985 **DIGITAL LOGIC DESIGN** ALAM, MANSAF,ALAM, BASHIR,2015-10-15 This textbook covers latest topics in the field of digital logic design along with tools to design the digital logic circuits It is designed for the undergraduate students pursuing courses in areas of engineering disciplines such as Electrical and Electronics Electronics and Communication Electronics and Instrumentation Telecommunications and Computer Science and Engineering It is also useful as a text for MCA M Sc

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Digital Logic Techniques T. J. Stonham, 1987 **Handbook of Digital Techniques for High-Speed Design**, 2007-09 **Foundations of Digital Logic Design** Gideon Langholz, Abraham Kandel, Joe L. Mott, 1998 This text is intended for a first course in digital logic design at the sophomore or junior level for electrical engineering computer engineering and computer science programs as well as for a number of other disciplines such as physics and mathematics The book can also be used for self study or for review by practicing engineers and computer scientists not intimately familiar with the subject After completing this text the student should be prepared for a second advanced course in digital design switching and automata theory microprocessors or computer organization

Fundamentals of Logic Design Charles H. Roth, 1992 *Digital Logic* M. Rafiquzzaman, Steven A. McNinch, 2019-09-11 Digital Logic with an Introduction to Verilog and FPGA Based Design provides basic knowledge of field programmable gate array FPGA design and implementation using Verilog a hardware description language HDL commonly used in the design and verification of digital circuits Emphasizing fundamental principles this student friendly textbook is an ideal resource for introductory digital logic courses Chapters offer clear explanations of key concepts and step by step procedures that illustrate the real world application of FPGA based design Designed for beginning students familiar with DC circuits and the C programming language the text begins by describing of basic terminologies and essential concepts of digital integrated circuits using transistors Subsequent chapters cover device level and logic level design in detail including combinational and sequential circuits used in the design of microcontrollers and microprocessors Topics include Boolean algebra and functions analysis and design of sequential circuits using logic gates FPGA based implementation using CAD software tools and combinational logic design using various HDLs with focus on Verilog

Digital Logic Design Gideon Langholz, Abraham Kandel, Joe L. Mott, 1988 **The Essence of Digital Design** Barry Wilkinson, 1998 Wilkinson provides a concise introduction to all the fundamental aspects of digital logic design covering state diagrams including those with transitional expressions and programmable logic devices He also looks at basic fault testing

Digital Logic Circuit Analysis and Design Victor Peter Nelson, 1995 For introductory digital logic design or computer engineering courses in electrical and computer engineering or computer science at the sophomore or junior level Many recent texts place instructors in the difficult position of choosing between authoritative state

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